

PATENT
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Applicant(s): Chang-Whan Jung, et al. Examiner: Nguyen, Minh
Serial No.: 09/879,637 Group Art Unit: 2816
Filing Date: June 12, 2001
Title: FUSE CIRCUIT FOR SEMICONDUCTOR INTEGRATED CIRCUIT

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AMENDMENT AFTER FINAL

Sir:

The following is in response to the Office Action dated April 7, 2003 and is filed within the shortened statutory period of three months. Please amend the application as follows.

In the specification:

Please amend the abstract of the invention as follows.

(Twice Amended) In a fuse circuit including programmable fuses in a semiconductor integrated circuit, the fuses store specific information related to the semiconductor integrated circuit, such as redundancy information, wafer lot number, die lot number, and die position on the wafer, etc. The fuse circuit utilizes a plurality of fuses for storing identical bit information. Consequently, in the case where a fuse has not been cut out correctly, the fuse circuit can reduce programming defects, whereby defect generation rates are remarkably decreased.